S/N 10/634,174

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes Examiner: Joannie A. Garcia

Serial No.: 10/634,174 Group Art Unit: 2823

Filed: August 05, 2003 Docket: 1303.102US1

Title: STRAINED Si/SiGe/SOI ISLANDS AND PROCESSES OF MAKING SAME

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

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Page 2 Dkt: 1303.102US1

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

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(612) 373-6960

Date 10 -19-04

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Reg. No. 38,377

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this _______ day of October, 2004.

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Approved for use through 10/31/2002. OMB 651-0031

US Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE

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(Use as many sheets as necessary) Sheet 1 of 5

Application Number	10/634,174	
Filing Date	August 5, 2003	
First Named Inventor	Forbes, Leonard	
Group Art Unit	2823	
Examiner Name	Garcia, Joannie	

Attorney	Docket	No:	1303.	.102US1
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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE 10/634,174 **Application Number** STATEMENT BY APPLICANT **Filing Date** August 5, 2003 (Use as many sheets as necessary) **First Named Inventor** Forbes, Leonard **Group Art Unit** 2823 Garcia, Joannie **Examiner Name** Attorney Docket No: 1303.102US1 Sheet 2 of 5

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Substitute for form 1449A/PTO	Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Application Number	10/634,174		
	Filing Date	August 5, 2003		
	First Named Inventor	Forbes, Leonard		
	Group Art Unit	2823		
	Examiner Name	Garcia, Joannie		
Sheet 3 of 5	Attorney Docket No: 1	303.102US1		

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Application Number	10/634,174			
	Filing Date	August 5, 2003			
	First Named Inventor	Forbes, Leonard			
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	Examiner Name	Garcia, Joannie			
Sheet 4 of 5	Attorney Docket No: 1	303.102US1			

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Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposlum, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE 10/634,174 **Application Number** STATEMENT BY APPLICANT August 5, 2003 **Filing Date** (Use as many sheets as necessary) Forbes, Leonard **First Named Inventor Group Art Unit** 2823 Garcia, Joannie **Examiner Name** Attorney Docket No: 1303.102US1 Sheet 5 of 5

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S/N 10/634,174 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes TRADEMAR

Examiner: Joannie A. Garcia

Serial No.:

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August 5, 2003

Docket: 1303.102US1

Title:

STRAINED SI/SIGE/SOI ISLANDS AND PROCESSES OF MAKING SAME

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No. 10/379749	Filing Date March 5, 2003	Attorney Docket 1303.089US1	Title MICRO-MECHANICALLY STRAINED SEMICONDUCTOR FILM
10/425797	April 29, 2003	1303.093US1	LOCALIZED STRAINED SEMICONDUCTOR ON INSULATOR
10/431134	May 7, 2003	1303.094US1	STRAINED Si/SiGe STRUCTURES BY ION IMPLANTATION
10/425484	April 29, 2003	1303.095US1	STRAINED SEMICONDUCTOR BY WAFER BONDING WITH MISORIENTATION
10/443355	May 21, 2003	1303.098US1	SILICON OXYCARBIDE SUBSTRATES FOR BONDED SILICON ON INSULATOR
10/443340	May 21, 2003	1303.099US1	ULTRA-THIN SEMICONDUCTORS BONDED ON GLASS SUBSTRATES
10/431137	May 7, 2003	1303.100US1	MICROMECHANICAL STRAINED SEMICONDUCTOR BY WAFER BONDING

COMMUNICATION CONCERNING RELATED APPLICATIONS
Serial Number: 10/634,174
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Page 2 Dkt: 1303.102US1

10/443337	May 21, 2003	1303.103US1	GETTERING OF SILICON ON INSULATOR USING RELAXED SILICON GERMANIUM EPITAXIAL PROXIMITY LAYERS
10/443339	May 21, 2003	1303.104US1	WAFER GETTERING USING RELAXED SILICON GERMANIUM EPITAXIAL PROXIMITY LAYERS
10/623794	July 21, 2003	1303.108US1	GETTERING USING VOIDS FORMED BY SURFACE TRANSFORMATION
10/623788	July 21, 2003	1303.109US1	STRAINED SEMICONDUCTOR BY FULL WAFER BONDING
10/164611	June 10, 2002		OUTPUT PREDICTION LOGIC CIRCUITS WITH ULTRA-THIN VERTICAL TRANSISTORS AND METHODS OF FORMATION
10/931554	August 31, 2004	1303.093US2	LOCALIZED STRAINED SEMICONDUCTOR ON INSULATOR
10/931749	August 31, 2004	1303.095US2	STRAINED SEMICONDUCTOR BY WAFER BONDING WITH MISORIENTATION
10/931344	August 31, 2004	1303.108US2	GETTERING USING VOIDS FORMED BY SURFACE TRANSFORMATION
10/931553	August 31, 2004	1303.099US2	ULTRA-THIN SEMICONDUCTORS BONDED ON GLASS SUBSTRATES
10/931580	August 31, 2004	1303.100US2	MICROMECHANICAL STRAINED SEMICONDUCTOR BY WAFER BONDING

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 10/634,174 Filing Date: August 5, 2003

Title: STRAINED SI/SIGE/SOI ISLANDS AND PROCESSES OF MAKING SAME

Page 3 Dkt: 1303.102US1

Respectfully submitted,

LEONARD FORBES

By Applicant's Representatives,

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Date___10-19-04

By

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Reg. No. 38,377

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 20 day of October, 2004.

Name

Signature